



JPW

Attorney Docket No.: 0553-0401

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Yoshifumi TANADA)
Serial No.: 10/807,692)
Filed: March 24, 2004)
For: Circuit For Inspecting Semiconductor)
Device And Inspecting Method)
Examiner:)
Art Unit:)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
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Shannon Wallace

Name of applicant, assignee, or Registered Rep.

Shannon Wallace 9/23/04
Signature Date

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT B

Prior to examination and calculation of fees, please amend the above-identified application as follows: